

**Amendments to the Drawings:**

The attached two replacement sheets of drawings include changes to Figures 1 and 2. These sheets replace the original sheets for the respective drawings. Annotated sheets showing changes to each of the two original sheets to be replaced are also provided with the changes shown as mark-ups in red ink.

Attachments: 2 Replacement Sheets  
2 Annotated Sheets Showing Changes

**REMARKS / ARGUMENTS**

The Examiner's objections to the drawings are addressed in two replacement sheets. The labels of "ACM" have been removed from the drawings. Claims 12 and 20 have been amended so that they do not recite "transposing" but rather "transferring". Hence there is no need to show transposing in the Figures.

Regarding the Section 101 and 112 rejections, the claims have been amended to remove references to "no latency."

Claims 1-20 remain pending in the application. Claims 1, 12 and 20 are the only independent claims and each includes a limitation not disclosed by nor made obvious in view of the prior art.


For example, each of claims 1, 12 and 20 is directed to a system that includes "a plurality of heterogeneous processing nodes". The nodes are "coupled by a programmable interconnection network having a network root and a plurality of crosspoint switches".

Note that Wilson's interconnection scheme employs a single crossbar switch 51 coupled to processing units such as 46, 47 and 48. See, e.g., Wilson's Fig. 5. Other switches such as 50 are not coupled to processing units. Also, there is no use of a "network root". Thus, Wilson teaches away from the recited architecture.

Okuda is concerned with a memory device and does not disclose processing units, processor interconnections, crosspoint switches or a network root.

Applicant respectfully requests that a timely Notice of Allowance be issued in this case. If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-279-5098.

Respectfully submitted,

  
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Date

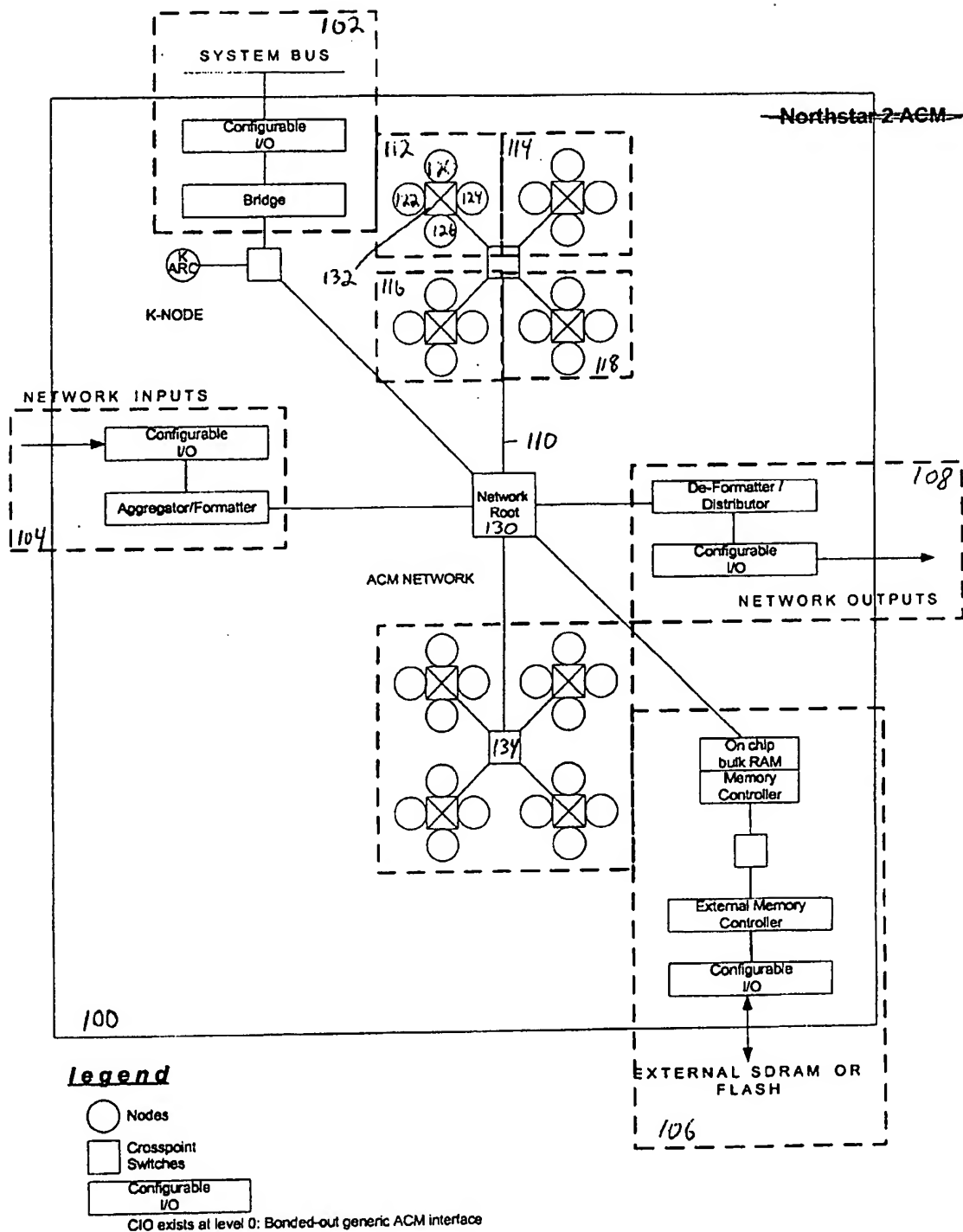


FIG. 1

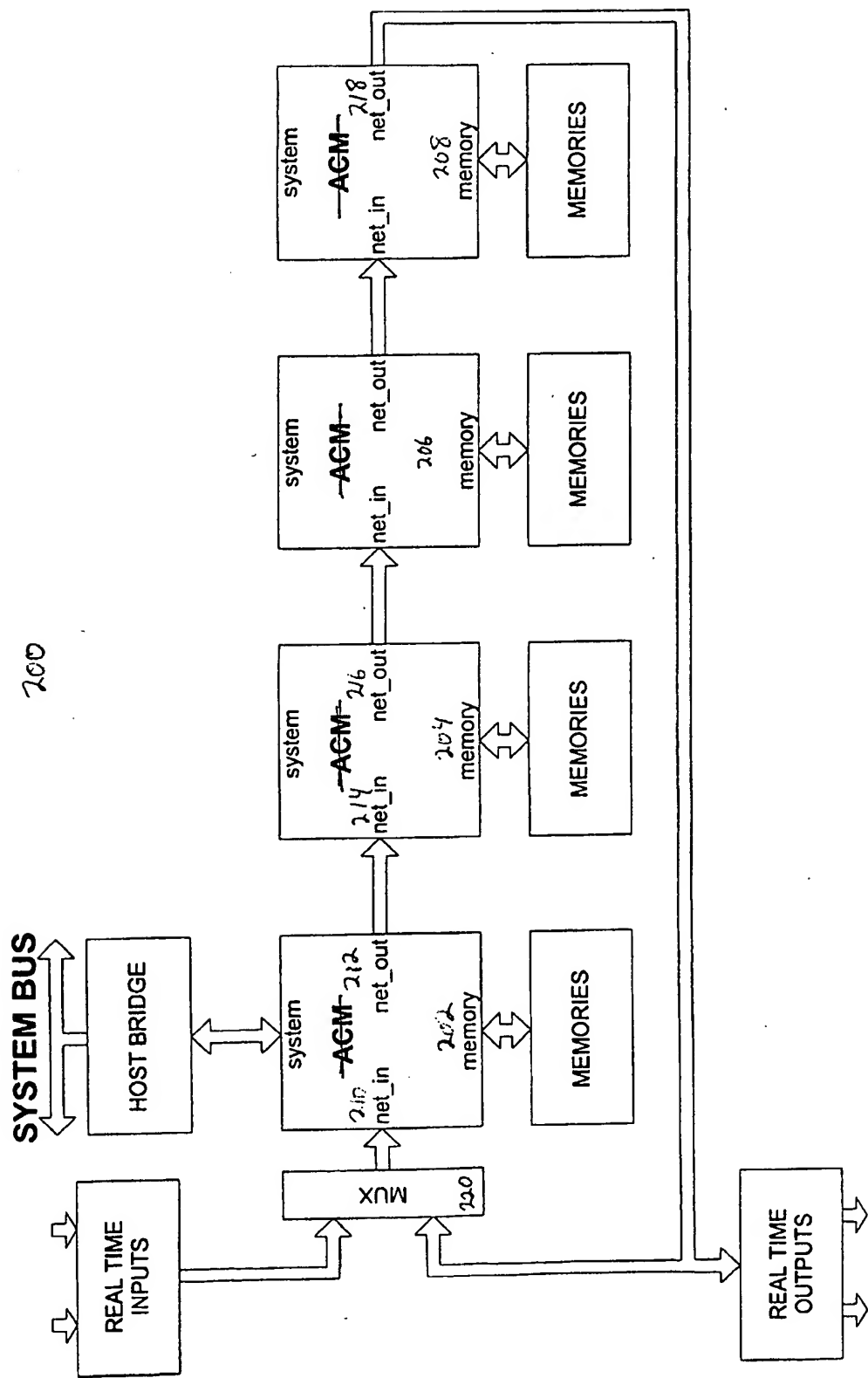


FIG. 2